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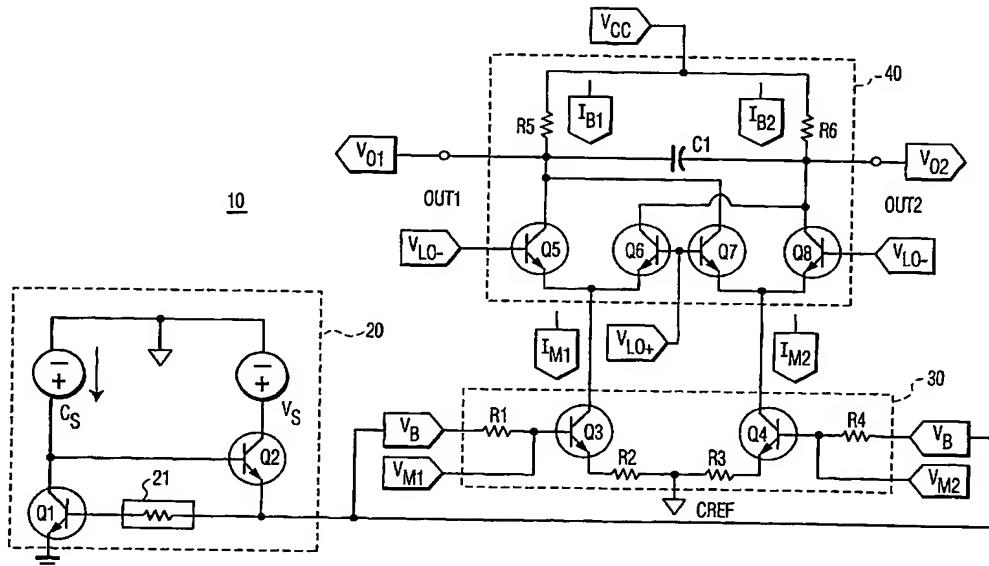
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(54) Title: MINIMIZING 1/F NOISE CONFIGURATION FOR ZIF MIXER



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(57) Abstract: A Gilbert cell (Q3-Q8) of a mixer (11) controls a differential output voltage between a pair of output terminals (OUT1, OUT2) of the mixer (11). A polysilicon resistor (R7) of the mixer (11) applies a differential loading to the differential output voltage. A pair of current sources (Q11, Q12) of the mixer (11) provide biasing currents to the Gilbert cell (Q3-Q8). A pair of resistors (R8, R9) of the mixer (11) impede a flow of DC current through the current sources (Q11, Q12), respectively.



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